

PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yoko HIRIGUCHI

Appln. No.:

Group Art Unit:

Filed: July 13, 2000

Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT

1/1a
12-31-00
Payton

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, kindly amend the above-identified application as follows:

IN THE CLAIMS:

~~Claim~~ 3, line 1, delete "claims 1 and 2" and insert --claims 1 or 2--.

~~Claim~~ 6, line 1, delete "claims 4 and 5" and insert --claims 4 or 5--.

~~Claim~~ 7, line 1, delete "claims "4 and 5" and insert --claims 4 or 5--.

~~Claim~~ 8, line 1, delete "claims "1 and 2" and insert --claims 1 or 2--.

~~Claim~~ 9, line 1, delete "claims 1 and 2" and insert --claims 1 or 2--.

~~Claim~~ 10, line 1, delete "claims 1 and 2" and insert --claims 1 or 2--.

~~Claim~~ 11, line 1, delete "claims 4 and 5" and insert --claims 4 or 5--.

~~Claim~~ 12, line 1, delete "claims 1 and 2" and insert --claims 1 or 2--.

~~Claim~~ 13, line 1, delete "claims 4 and 5" and insert --claims 4 or 5--.

~~Claim~~ 14, line 1, delete "claims 1 and 2" and insert --claims 1 or 2--.